



PATENT APPLICATION

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 2414

KANAMORI et al.

Art Unit: 2816

Application No.: 10/072,872

Examiner: L. Nguyen

Filed: February 12, 2002

Attorney Dkt. No.: 100021-00069

For: DIFFERENTIAL AMPLIFIER CIRCUIT CAPABLE OF ACCURATELY
AMPLIFYING EVEN HIGH-SPEEDED SIGNAL OF SMALL AMPLITUDE

RESPONSE UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

Date: February 12, 2003

Sir:

This paper is filed in response to the Office Action dated September 12, 2002, the time period for response having been extended until February 12, 2003 by the enclosed Petition for Extension of Time. In the prosecution of the application identified above, please consider the following amendments.

IN THE CLAIMS:

Please cancel claims 2, 7, 21 and 26.

Please amend claims 1, 3, 4, 5, 8, 11, 15, 17, 22, 23, 27, 30 and 34 as follows:

1. (Amended) A differential amplifier circuit comprising a latch unit and a differential input portion, wherein:

said differential input portion comprises a first transistor and a second transistor each having a first electrode, a second electrode and a control electrode;

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